



S/N: 10/776,054
Docket No.: UMT-359
Title: COLUMN READ AMPLIFIER POWER-GATING
TECHNIQUE FOR INTEGRATED CIRCUIT MEMORY DEVICES
AND THOSE DEVICES INCORPORATING EMBEDDED DYNAMIC
RANDOM ACCESS MEMORY (DRAM)
Inv: Kim C. Hardee
Replacement Sheet

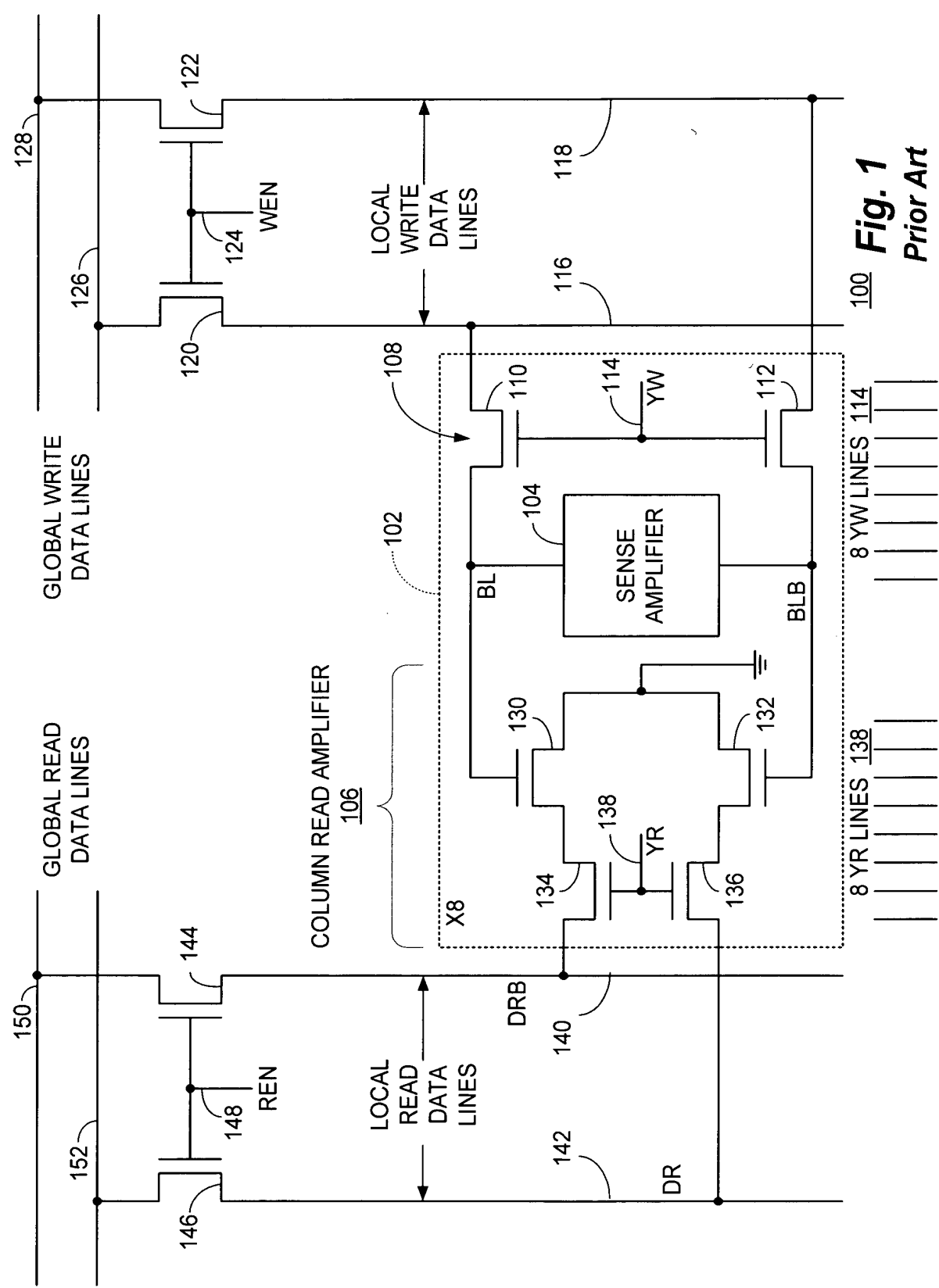


Fig. 1
Prior Art

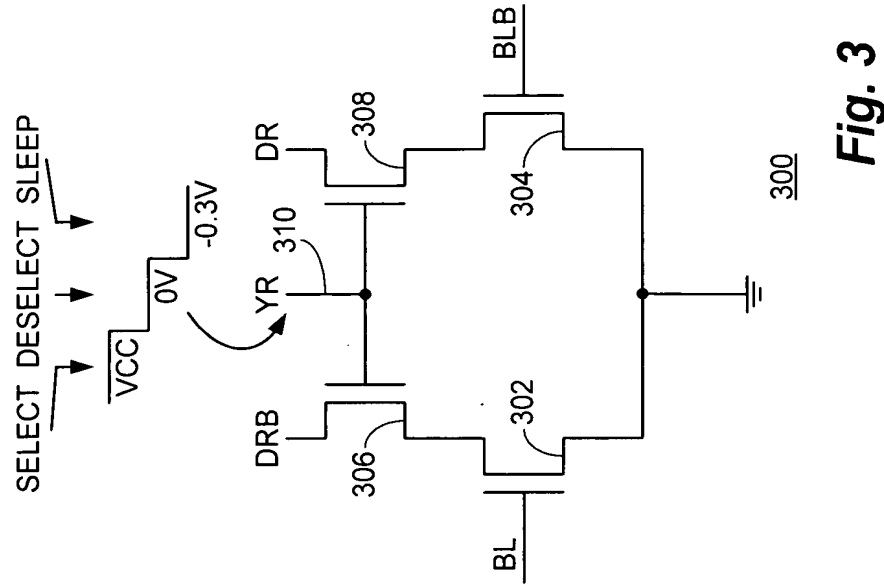


Fig. 3

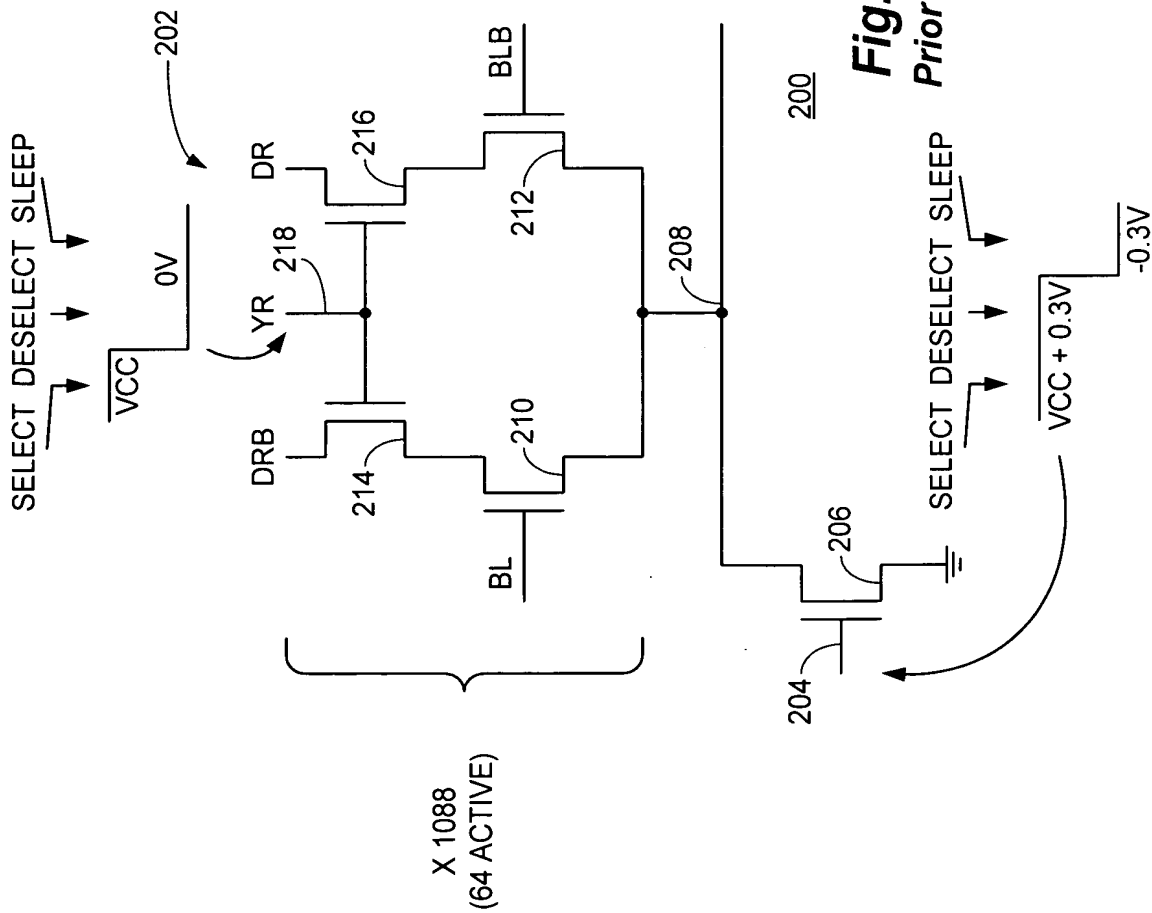


Fig. 2
Prior Art